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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,694,202 *B2*

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It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 11, line 27, in Claim 1, delete "lines;" and insert - - lines: - -, therefor.

In column 12, line 32, in Claim 11, delete "read." and insert - - read during said data write/read operation. - -, therefor.

In column 13, lines 10-11, in Claim 18, delete "bits and a plurality of memory cells to store data" and insert - - bits; - -, therefor.